

Description

[METHOD OF REWORKING INTEGRATED CIRCUIT DEVICE]

CROSS REFERENCE TO RELATED APPLICATIONS

[0001] This application claims the priority benefit of Taiwan application serial no. 92120185, filed July 24, 2003.

BACKGROUND OF INVENTION

[0002] Field of the Invention

[0003] The present invention relates to a reworking method, and more particularly to a method of reworking integrated circuit devices.

[0004] Description of the Related Art

[0005] The fabrication process of integrated circuit devices is very complex. Usually, integrated circuit devices can be fabricated by hundreds of different process steps. Once requirement of a deposition, etching or lithographic process cannot be achieved during the fabrication process, wafers should be reworked or scraped.

[0006] For example, during the process of formation of metal interconnect, usually a barrier layer, a metal layer and an anti-reflective layer are sequentially formed on a substrate having a dielectric layer thereon; then, the definition of the pattern is performed by lithographic and etching processes. During the deposition of the barrier layer, the metal layer, or the anti-reflective layer, if particle contamination or due to other undesirable reason will make the implementation of certain process steps impossible, rework is therefore required. However, when the steps of rework are so complex, time-consuming, high-cost or impossible to achieve reliability requirement, then wafers should be scraped. However, scraping of wafers will increase cost.

SUMMARY OF INVENTION

[0007] Accordingly, one object of the present invention is to provide a method of reworking integrated circuit devices, which method is capable of removing the material layer on a substrate requiring reworking unaffecting device reliability.

[0008] A further object of the present invention is to provide a method of reworking integrated circuit device, which method is fast, efficient and economical.

[0009] In accordance with the above objects, as broadly embodied and described herein, the present invention provides a method of reworking integrated circuit devices. A substrate having a dielectric layer, a barrier layer, a conductive layer and an anti-reflective layer formed thereon, is provided. The method of reworking the barrier layer, the conductive layer and the anti-reflective layer comprises removing the anti-reflection layer by a dry etching process, removing the conductive layer by a wet etching process, and then removing the barrier layer by a chemical mechanical polishing process.

[0010] According to one embodiment of the present invention, the material of the anti-reflective layer is selected from a group consisting of Ti/TiN, amorphous silicon and Ta/TaN, and the dry etching process for removing the anti-reflective layer is performed using a gas source selected from a group consisting of Cl_2 , BCl_3 , hydrocarbon containing halogen and SF_6 . In addition, when the material of the conductive layer includes Al, Al-Cu alloy or Al-Si-Cu alloy or tungsten, the wet etching process is performed using a solution containing sulfuric acid as an etching solution. When the material of the conductive layer includes Cu, the wet etching process is performed using a solution

containing nitric acid as an etching solution.

[0011] The method of the present invention does not affect device reliability and can be implemented quickly, efficiently and economically for removing the material layers on the substrate require to be reworked.

BRIEF DESCRIPTION OF DRAWINGS

[0012] FIGS. 1 to 4 are cross-sectional views showing an exemplary embodiment of reworking integrated circuit devices in accordance with the present invention.

[0013] FIG. 5 is a flowchart showing an exemplary embodiment of reworking integrated circuit devices in accordance with the present invention.

DETAILED DESCRIPTION

[0014] Referring to FIG. 1, the method of reworking integrated circuit devices in accordance with the present invention is suitable for reworking semiconductor devices. A substrate 100, having, for example from bottom to top, a dielectric layer 102 with a plug 104 embedded therein, a barrier layer 102, a conductive layer 108 and an anti-reflective layer 110 formed thereon is provided. The materials of the barrier layer 102 and the anti-reflective layer 110 are, for example, Ti/TiN, Ta/TaN, WN or amorphous silicon. The

material of the conductive layer 108 includes, for example, Al, Al-Cu alloy, Al-Si-Cu alloy, tungsten, or Cu. The rework method of the present invention will be described in detail as follows: Referring to FIG. 2 and FIG. 5, a dry etching process (step 210) is performed to remove the anti-reflective layer 110. The dry etching process for removing the anti-reflective layer 210 is performed using a gas source selected from a group consisting of Cl_2 , BCl_3 , hydrocarbon containing halogen and SF_6 , wherein the hydrocarbon containing halogen comprises alkane containing fluorine, such as CHF_3 , or alkane containing chlorine, such as CCl_4 .

[0015] Referring to FIG. 2 and FIG. 4, a wet etching process (step 220) is performed to remove the conductive layer 108. When the material of the conductive layer 108 is Al, Al-Cu alloy, Al-Si-Cu alloy, or tungsten, the wet etching process for removing the conductive layer 108 is performed using an etching solution containing sulfuric acids. When the conductive layer comprises Cu, the wet etching process for removing the conductive layer 108 is performed using an etching solution containing nitric acid.

[0016] Then, referring to FIG. 4 and FIG. 5, a chemical mechanical polishing process (step 230) is performed to remove the

barrier layer 106. The material of the barrier layer 106 is, for example, Ti/TiN, Ta/TaN, WN or amorphous silicon, wherein the chemical mechanical polishing process is performed using a slurry containing aluminum oxide, hydrogen peroxide, and ammonium hydroxide.

[0017] By experimental proof, the reworking method of the present invention actually does not affect device reliability, and can be fast, efficient and economical. Therefore, the method of the present invention is capable of reducing the rework cost, and also avoid scrapping of wafers cost due to failure of reworking.

[0018] Although the present invention has been described in terms of an exemplary embodiment, it is not limited thereto. Rather, the appended claims should be constructed broadly to include other variants and embodiments of the invention which may be made by those skilled in the field of this art without departing from the scope and range of equivalents of the invention.